

This document contains illustrated information about significant new features introduced with **Bartels AutoEngineer Version 6.6**.

Bartels AutoEngineer Version 6.6 Highlights Published by: Bartels System GmbH, Erding

Last printing: October 2005

The information contained in the **Bartels AutoEngineer** publications as well as the products and/or programs described therein are subject to change without notice and should not be construed as a commitment by Bartels System.

Although Bartels System has gone to great effort to verify the integrity of the information provided with the **Bartels AutoEngineer** publications, these publications could contain technical inaccuracies or typographical errors. Bartels System shall not be liable for errors contained therein or for incidental consequential damages in connection with the furnishing, performance or use of this material. Bartels System appreciates readers' and/or users' comments in order to improve these publications and/or the products described therein. Changes are periodically made to the information therein. These changes will be incorporated in new editions of the **Bartels AutoEngineer** publications.

All rights reserved. No part of the **Bartels AutoEngineer** publications may be reproduced, stored in a retrieval system, translated, transcribed or transmitted, in any form or by any means manual, electric, electronic, electromagnetic, mechanical, chemical, optical or otherwise without prior express written permission from Bartels System.

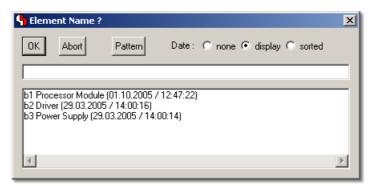
Bartels AutoEngineer®, Bartels Router® and Bartels Autorouter® are registered trademarks of Bartels System. Bartels User Language[™] and Bartels Neural Router[™] are trademarks of Bartels System. All other products or services mentioned in this publication are identified by the trademarks or service marks of their respective companies or organizations.

> Copyright © 1986-2005 by Oliver Bartels F+E All Rights Reserved Printed in Germany

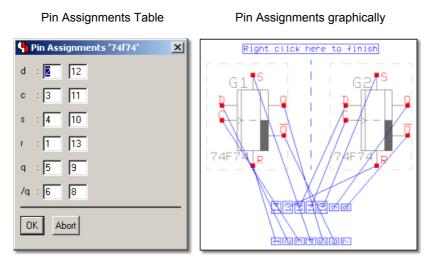
Contents

Element Selection with Comment and Date Display	5
Symbol Logic Editor with interactive Pin Assignment	5
Automatic Bus Definition for Symbol Bus Pin Connections	
Symbol Database with Symbol Preview	
Symbol Context Menus programmable through Symbol Library Definitions	
Toolbar Text Placement Function with Text/System Attribute List	
Warnings issued when connecting named Nets	
Toolbar with Element Access History and Preference Layer Display	
Horizontal/Vertical Measuring Function	7
Trace Antenna Recognition	7
Element Manipulation Dialogs through p Key	8
Autorouter with improved SMD Pin Routing Algorithms	
PDF Output with Layer Info (for Adobe Reader 6+)	
Dialog for Net Group DRC Assignments (BAE HighEnd)	

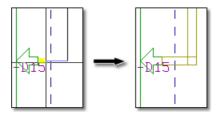
Element Selection with Comment and Date Display



Symbol Logic Editor with interactive Pin Assignment



Automatic Bus Definition for Symbol Bus Pin Connections



Symbol Database with Symbol Preview

Symbol ad/541a ad/552 ad/5741a ad/5765 dac312 adr228 dac811 dac7800 dac7801 dac7801 dac7801 dac7801 ltc1257 ltc1459 ltc1650 ltc1660 ltc1661		Comment:Category Dv& Converter (CHOS-12-Bit, Cur. Out.),Dv& Converters Dual Dv& Converter (12-Bit),Dv& Converters Dual Dv& Converter (12-Bit),Dv& Converters Dv& Converter (12-Bit),Dv& Converters Dv& Converter (12-Bit,Dv& Converters Dv& Converter (12-Bit, Dv Conpatible),Dv& Converter Dual CMCS DwC (12-Bit, Nultplying),Dv& Converters Dv& Converter (12-Bit, Sersial),Dv& Converters dual Serial Dv& Converter (10-Bit),Dv& Converters Cotal Serial Dv& Converter (10-Bit),Dv& Converters Nicropower Dv& Converter (10-Bit),Dv& Converters Quad Serial Dv& Converter (10-Bit),Dv& Converters Nicropower Dv& Converter (12-Bit),Dv& Converters Quad Serial Dv& Converter (12-Bit),Dv& Converters Quad Serial Dv& Converter (12-Bit),Dv& Converters		Likrey: ed Symbol: ed5582
--	--	--	--	------------------------------

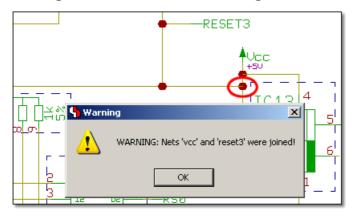
Symbol Context Menus programmable through Symbol Library Definitions

Pantala OutoFrainces COF	SYSDEMO		
Bartels AutoEngineer CAE	STSDEMU	Set Header Attributes	
Engineer: Date: 29.03.2005	File: su Element: bi		
		Load Macro	
All rights reserved	HEMM Pa	Load Plan from Project	
		Abort	

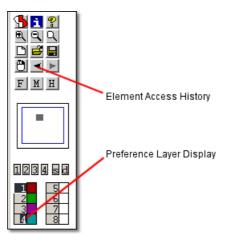
Toolbar Text Placement Function with Text/System Attribute List

\$ - Physical/Layout name		
\$\$ - Logical/Schematic name		
gp - Gate Pin/Gate selection		
Iname - Logical Library Name/Symbol macro name		
Snople - Placement status		
Spiname - Physical Library Name/Layout part macro r	ame	
Spitecomment - Element comment		
\$pltename - Element/plan name lower case		
\$Pltename - Element/plan name upper case		
\$pltdateus - Current date, year 4 digits		
Spltdate2us - Current date, year 2 digits		
Splttime - Current time		
pltfbname - Project file name with path, without DDB	extension	
Pltfbname - Project file name with path, without DDB	extension upper case	
pltfbsname - Project file name without path, without I	DDB extension	
Pltfbsname - Project file name without path, without		
Coltfname - Project file name with path and DDB exter		
Pltfname - Project file name with path and DDB exte		
pltfsname - Project file name without path, with DDB		
Pltfsname - Project file name without path, with DDB		
Spltsdateus - Save date, year 4 digits		
Spltsdate2us - Save date, year 2 digits		
Spltstime - Save time		
Spltpname - Packager last netlist name lower case		
Pltpname - Packager last netlist name upper case		
Spltpdateus - Packager date, year 4 digits		
phpdateds in ackager date, year 4 digits		
Spltptime - Packager time		
proprinte - rackager une Glext - Requested Library Extension/Name extension	for logical definition	
Sriname - Requested Library/Name of logical definition		
Sinname - Requested Ebrary/Name of logical definition		-
Nonade - Beddested Fad Nadezi avolunaŭ hade		
OK Abort		

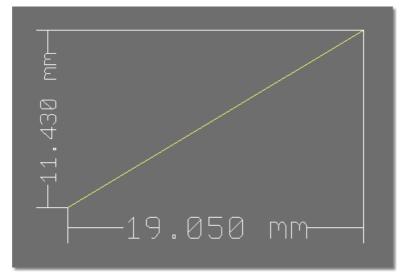
Warnings issued when connecting named Nets



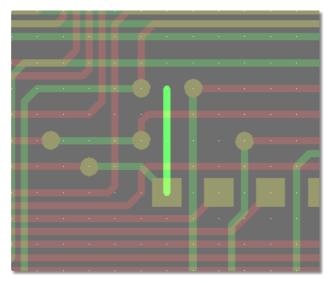
Toolbar with Element Access History and Preference Layer Display



Horizontal/Vertical Measuring Function



Trace Antenna Recognition

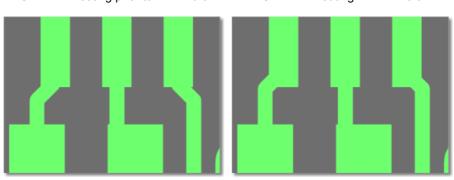


Element Manipulation Dialogs through p Key

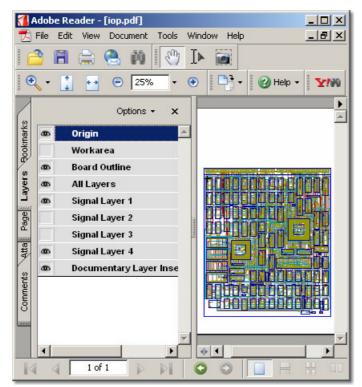
👆 Set Data	×
Polygon Type :	Copper Fill Area
Sub Type :	
Layer	Signal Layer 3
X:	44.45
Y:	52.07
Net Name :	@20 Browse
Height :	
Pen Width :	
Fixation :	⊙ gff O gn
Glue :	⊙ <u>o</u> ff O <u>o</u> n
<u>OK</u> <u>A</u> bort	⊙ <u>m</u> m C_Inch

👆 Set Data								×
	Drill							
X:	0.0							
Y:	0.0							
Diameter :	0.02							
Drilling Class :		ŀ	•					
Mirror Drilling Class	:	ŀ	•					
Connection to powe	er layers :						All	None
	3 🔽 4	₽ 5	₽ 6	7	▼ 8	м э	▼ 10 ▼	11 🔽 12
Direct connect to power layer : All None								
	3 🗖 4	5	□ 6	□ 7	□ 8	9	□ 10 □	11 🗖 12
Fixation :	• off	O on						
Glue :	 off 	O on						
OK Abort		C mn	n •	Inch				

Autorouter with improved SMD Pin Routing AlgorithmsSMD Pin Routing prior to BAE V6.6SMD Pin Routing in BAE V6.6



PDF Output with Layer Info (for Adobe Reader 6+)



Dialog for Net Group DRC Assignments (BAE HighEnd)

Set group DRC blocks					
lv	std				
hv : 2	1				
lv :	a				
OK Abort					

Please see the **Bartels AutoEngineer® Version 6.6 Release Notes** for a detailed description of all new **BAE Version** 6.6 features.