

This document contains illustrated information about some of the new features introduced with **Bartels AutoEngineer Version 7.0**.

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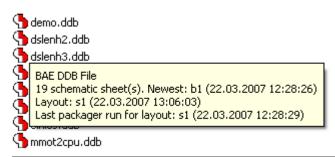
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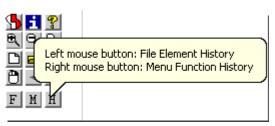
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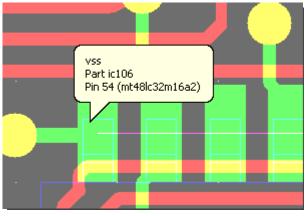
Bartels AutoEngineer Tooltips (Windows)





Toolbar Function Tooltips

File Contents Info during File Selection

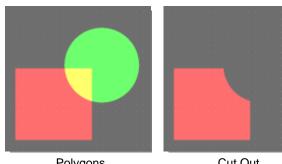


Element Info (optional)

Schematic Editor - Symbol Logic Editor Dialog for Alternative Package Type Assignments

| Layout Part Macro : | dil8,so8 | | | |
|--|--|---|--|--|
| Apply Pattern : | so8 | | | |
| Part Macros in Project File : | Library Part Macros 'lay | /lib' : | | |
| s2512 screw_m3_contact sil2 sil3 sil6 sil8 sma_edge sma_hs45111 smc so14 so16 so16 so16 so16 so16 so20 so28 so20 so28 so20 so28 so4323 sod6 sod80 so480 so480 sot143 co1222 | 154smd 60a2 78lxx 78xx a_s1206 accu36 accu_vl23xx adjhole adjmark akku al_elko_va_a al_elko_va_b al_elko_va_b al_elko_va_d al_elko_va_d al_elko_va_f al_elko_va_f al_elko_va_g amd10pol amd4pol amd6pol amd8pol | amdsnap2 amdsnap4 amdsnap6 amdsnap8 amp_185683 amp_4_179373 amp_modu_s50_80ps ampqlok8 b78334_er11 bga256 bga256bc bga272 bga272ball bga304 bga320ball bga388ball bga432ball bga43ball bga4ball bga4ball bga4ball bga4 | bmark_r bnc bncsub boardspec bohrung_3.0 bsx c02c64 c02d63 c02r25 c02r30 c02r50 c03r25 c03r45 c04r50 c04r50 c04r50b10 c04r60b10 c05r50 c06r75 c06r75b10 c06r85 | |

Layout Editor Polygon Combination

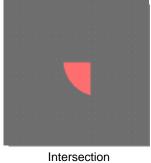


Polygons



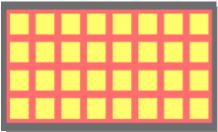


Contour



Layout Editor Thermal Stencil Pad Generation

| Tile Settings | X |
|---------------------|----------------------|
| Tile X Count : | 8 |
| Tile Y Count : | 4 |
| Tile Coverage [%] ? | 50 |
| Polygon Mode : | Area C Keep Out Area |

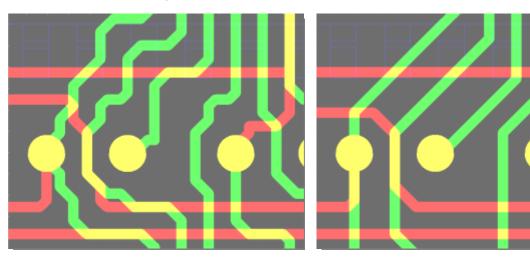


| Net Functions | | | | |
|---|---------------|------------|--|--|
| Copper Fill Area | Active Copper | Select | | |
| Layer No Layer | Deselect | | | |
| — Highlight | Color Net | No color 💌 | | |
| Toggle Net | Toggle Focus | Reset All | | |
| Toggle Airline | Enable All | Reset All | | |
| Zoom to | Zoom Last | Zoom All | | |
| \$ - Attribute Selecti /abe0 4 Pins /abe1 4 Pins /abe2 3 Pins /abe3 3 Pins /ams0 3 Pins /ams1 3 Pins /ams3 4 Pins /ams3 4 Pins /ace 5 Pins /are 5 Pins /awe 5 Pins /cpu_bg 2 Pins /cpu_bg 2 Pins /cpu_bg 7 Pins | on | | | |
| /cpuemu 3 Pins /enrecvadc 3 Pins /fe0_leda 2 Pins | | ~ | | |

Layout Editor Net Assistant

Modeless Layout Editor dialog with net-specific functions.

Layout Editor Trace Beautifier



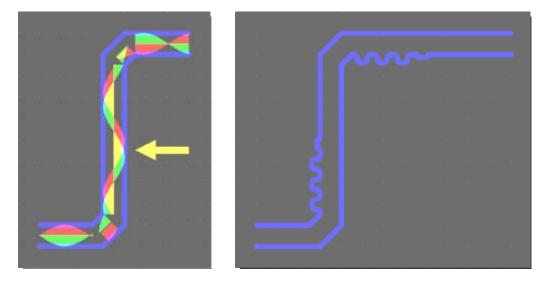
| 👆 Trace Length Adjustn | nent | × | |
|--------------------------|-----------------------------|---|--|
| C 45 Degree Corners | | | |
| Arcs | | | |
| New Trace Length : | C left C right C alternatin | g | |
| Step Length (I): | 0.7 | | |
| Max. Chamfer (r) : | 0.2 | | |
| Max. Elevation (a): | 0.5 | | |
| Min. Elevation (a): | 0.2 | | |
| <u>D</u> K <u>A</u> bort | ⊙ <u>m</u> m C mi] | | |

Layout Editor Trace Length Adjustment

Parallel Trace Signal Phase

Signal Phase Shift Display

Phase Shift Compensation through Trace Length Adjustment



Please see the **Bartels AutoEngineer**® **Version 7.0 Release Notes** for a detailed description of all new features introduced with **Bartels AutoEngineer Version 7.0** features.